DELFINO 168 Pin Control Card (BH II)

V0.3 to V0.4 Changes (by Matthew Webster)

p2 - Updated parts (values and footprints in bom)
Y1,C1, and C2; Changed RN2 - RN8 to 33 Ohms

p3 - Updates parts (type, and footprints) L22,L23,C9,C10
C38,C39,C43,C44,C17,C15,C24,C20,R10,R15,R16,
R13,R14,R205

p4 - Untangled GPIO 84-87 to make page easier to read;
Updated parts (type and footprints) for R193,R191,R190,
R192,R17, R18

V0.4 to V0.5 Changes (by Matthew Webster)

P2 - Updated Parts C1 and C2
P3 - Updated parts C12, C13, and C14
P5 - Updated pinout on parts U19 and U20

V0.5 to V0.6 Changes (by Matthew Webster)

P3 - Modified Delfino ZEP package power pin
arrangement (U1); Added inductors L24 thru L32 to
VDD, VDDIO, and VDD18

V0.6 to V0.7 Changes (by Matthew Webster)

P6 - Added 2.2k Reistor (R199) pullup to TCK.

V0.7 to V0.8 Changes (by Matthew Webster)

P2
- Added 33 ohm resistor to GPIO_38 net
- Added 33 ohm reistor to XWE1n net

V0.8 to V1.0 (by Matthew Webster)
- NO CHANGES

V1.0 to V1.1 Changes (by Matthew Webster)

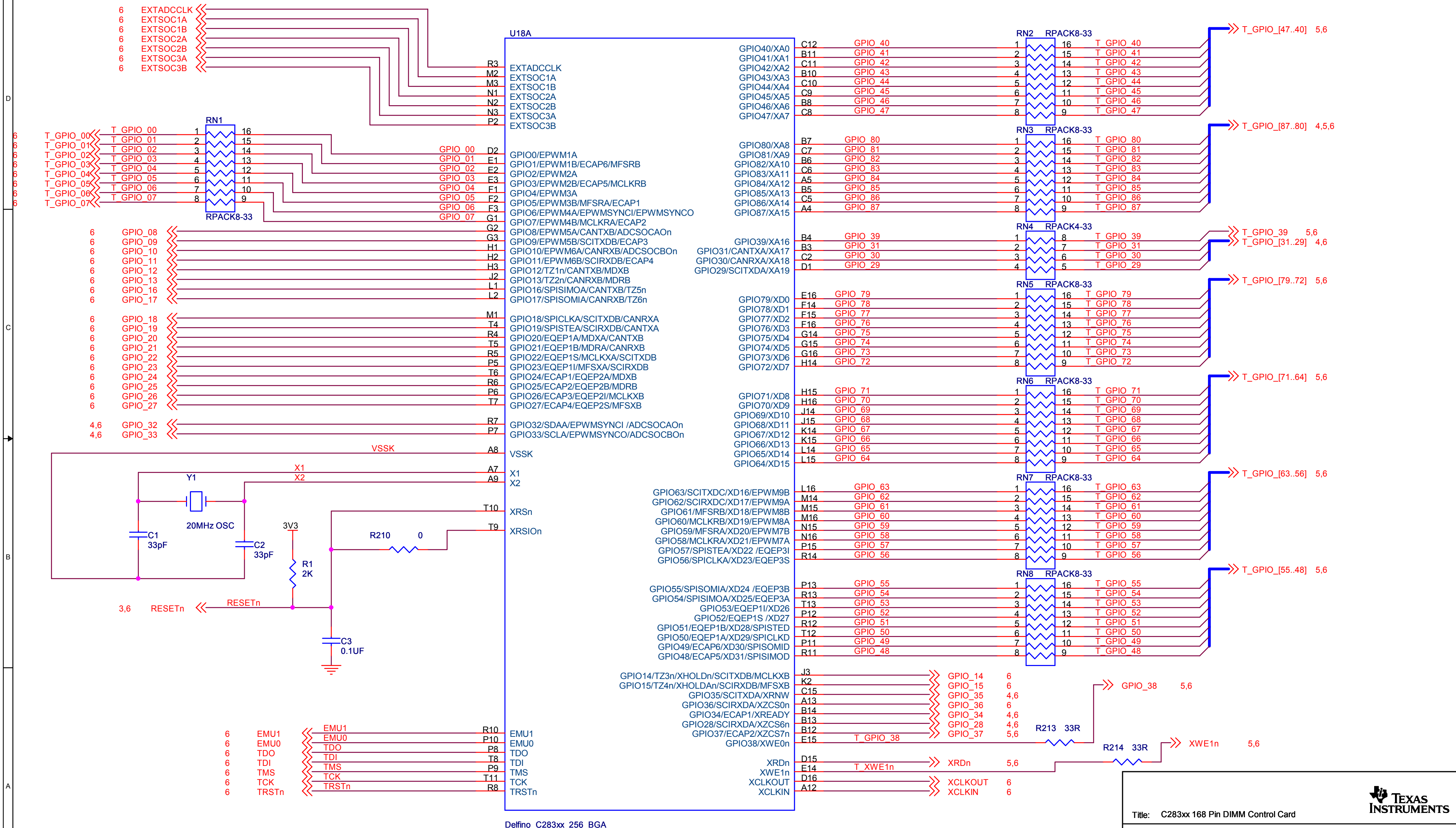
P3
- Reconnect 1.8V Power place that was cut by
row of VIAs.

P4
- Added (R215) 4.7k ohm resistor pullup
to GPIO_33 (SCL)
- Added (R216) 4.7k ohm resistor pullup
to GPIO_32 (SDA)
-Changed input to U6, pin1 to GPIO_35
from T_GPIO_31

P5
-Changed connection to R211 to GPIO_37
from GPIO_36
-Edited Memory pinout, bits A1 and A2
were swapped. (so GPIO_42/43 were
swapped)

TEXAS INSTRUMENTS

Title:	C283xx 168 Pin DIMM Control Card
Page Contents:	Title Page
Size: B	C283XX_B2H_CONTROLLERCARD_V1_1.DSN
Revision:	1.1
Date:	Wednesday, February 04, 2009
Sheet	1 of 7



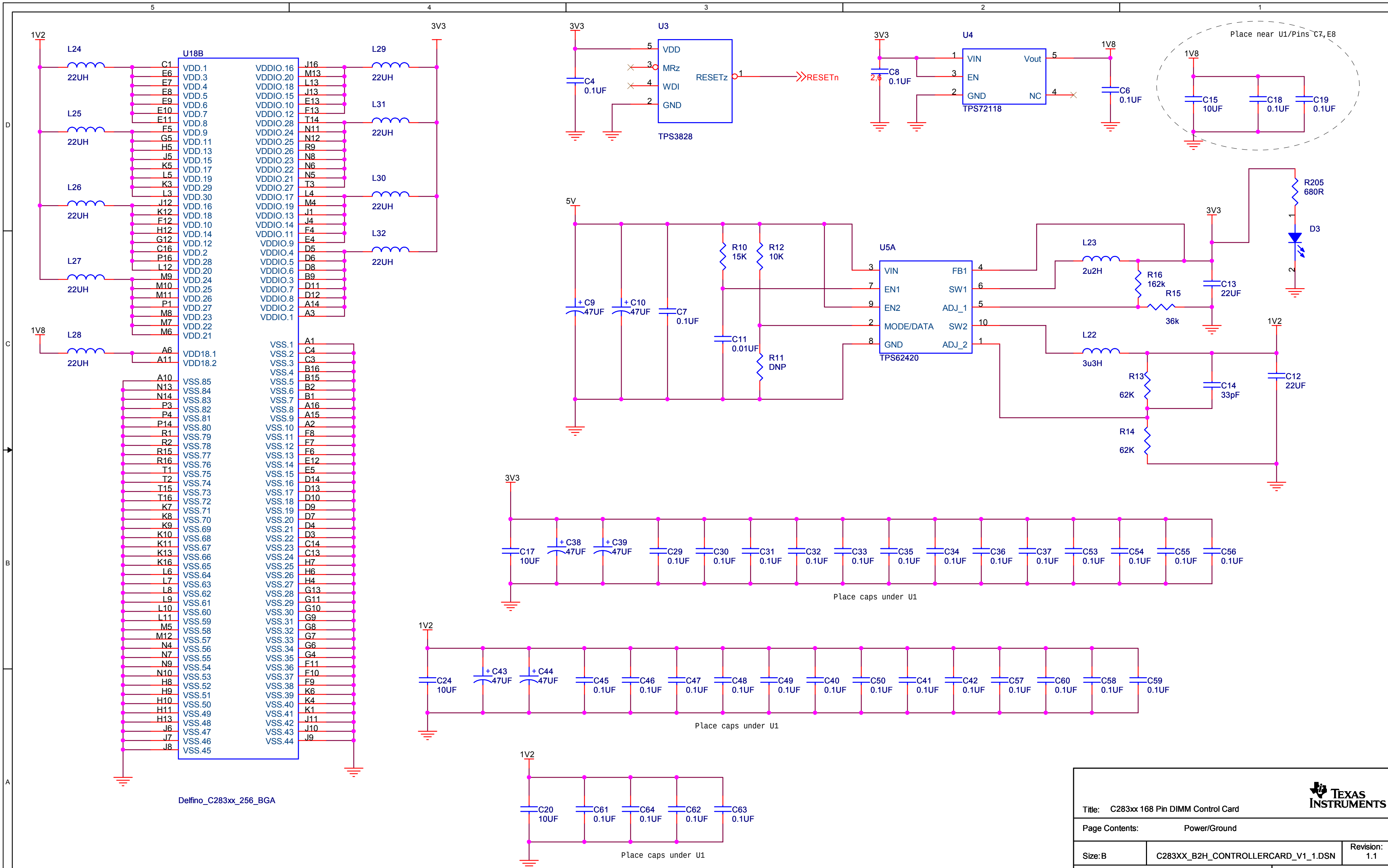
Title: C283xx 168 Pin DIMM Control Card

Page Contents: MCU

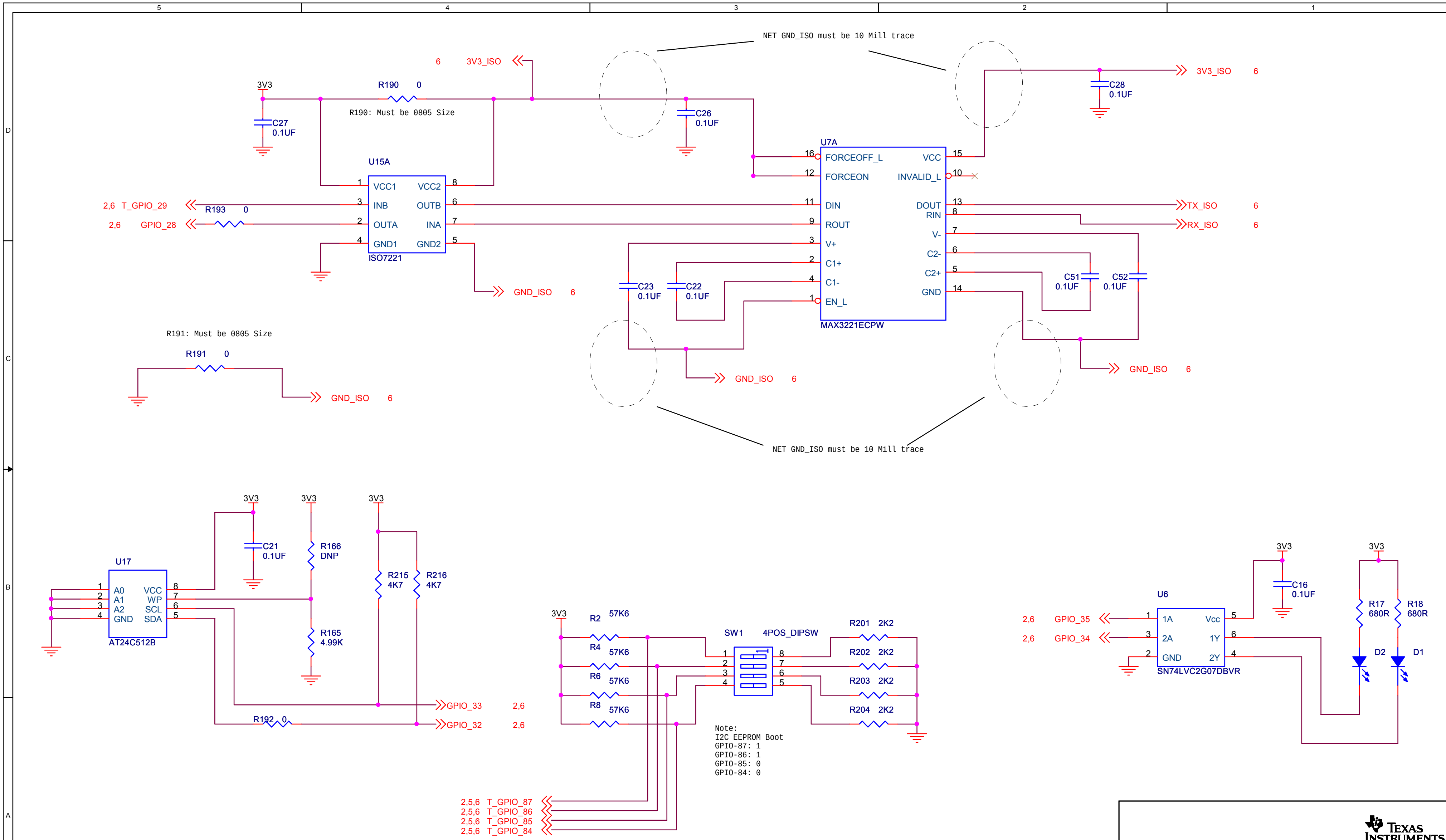
Size: B | C283XX_B2H_CONTROLLERCARD_V1_1.DSN | Revision: 1.1

Date: Wednesday, February 04, 2009 | Sheet 2 of 7

TEXAS INSTRUMENTS

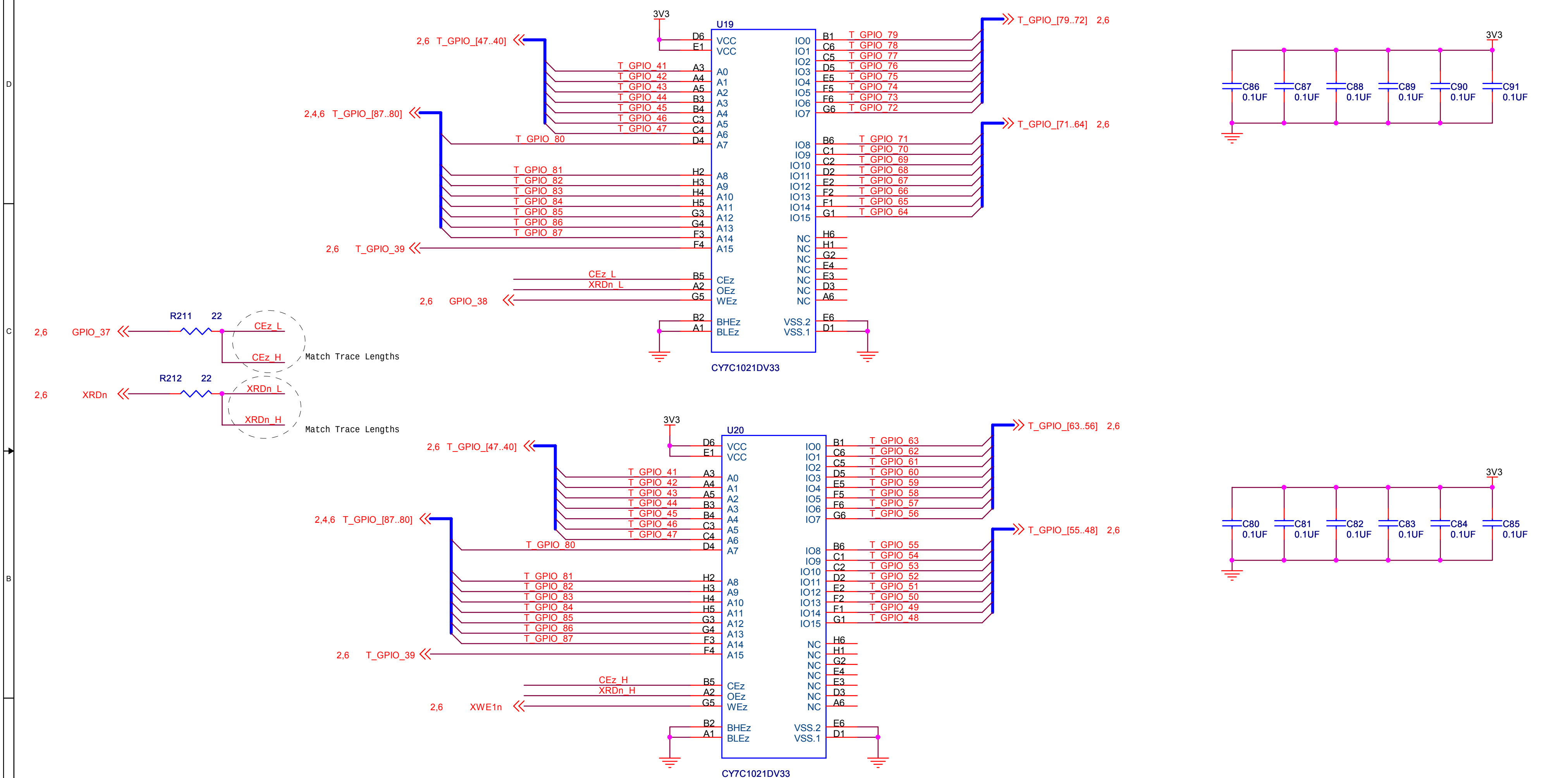


Title: C283xx 168 Pin DIMM Control Card		TEXAS INSTRUMENTS
Page Contents: Power/Ground		
Size: B	C283XX_B2H_CONTROLLERCARD_V1_1.DSN	Revision: 1.1
Date: Wednesday, February 04, 2009	Sheet 3 of 7	

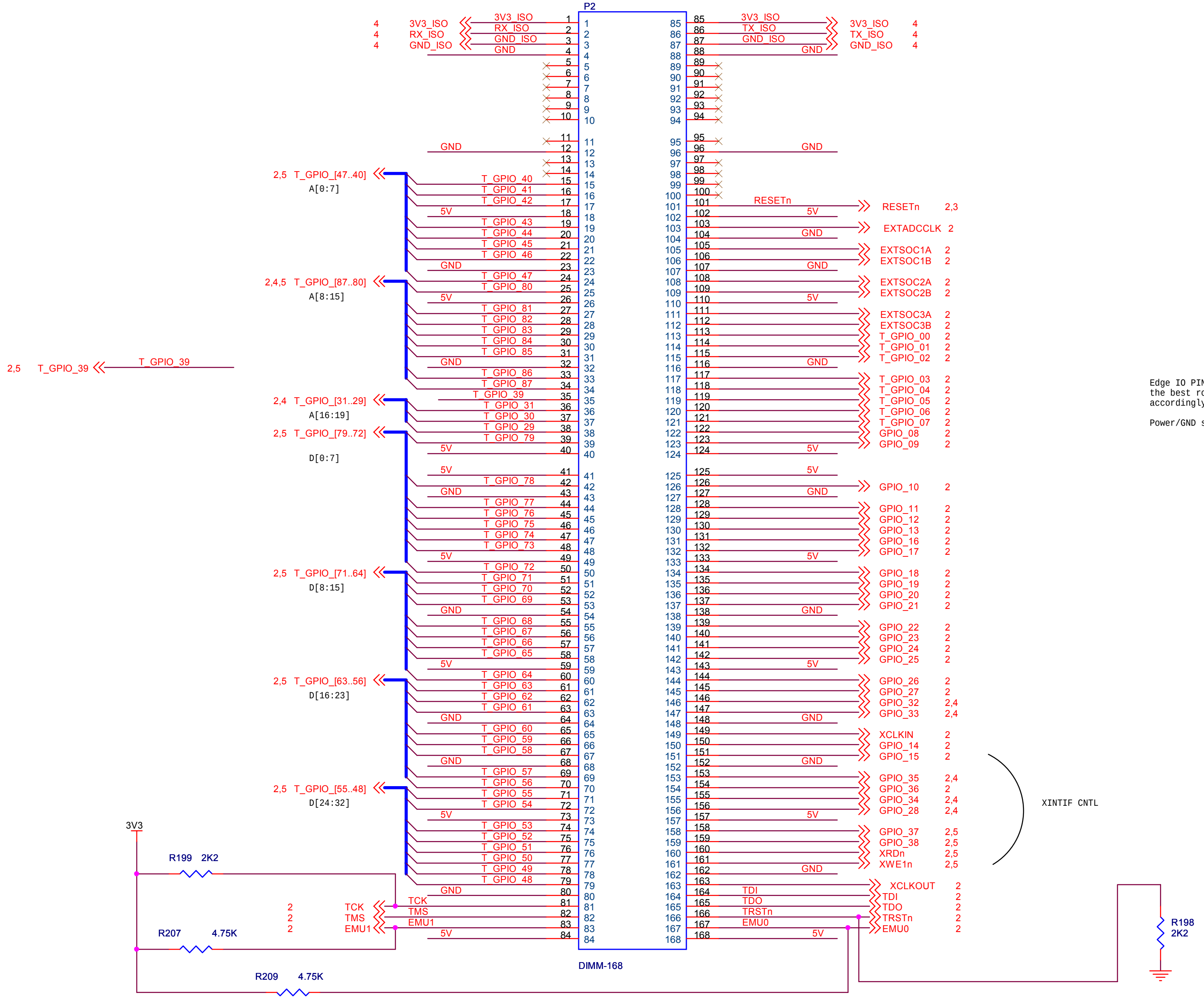


TEXAS INSTRUMENTS

Title: C283xx 168 Pin DIMM Control Card			
Page Contents: IO			
Size: B	C283XX_B2H_CONTROLLERCARD_V1_1.DSN		Revision: 1.1
Date:	Wednesday, February 04, 2009	Sheet 4 of 7	



Title:	C283xx 168 Pin DIMM Control Card
Page Contents:	External Memory
Size: B	C283XX_B2H_CONTROLLERCARD_V1_1.DSN — Revision: 1.1
Date:	Wednesday, February 04, 2009 — Sheet 5 of 7



Edge IO PINOUT is not FIXED (yet), let's determine the best route for the the XINTF signals and modify accordingly.

Power/GND signals should stay the same.

Title: C283xx 168 Pin DIMM Control Card			TEXAS INSTRUMENTS
Page Contents: Connector			
Size: B	C283XX_B2H_CONTROLLERCARD_V1_1.DSN		Revision: 1.1
Date:	Wednesday, February 04, 2009	Sheet 6 of 7	

Title: C283xx 168 Pin DIMM Control Card		TEXAS INSTRUMENTS
Page Contents: Notes		
Size: B	C283XX_B2H_CONTROLLERCARD_V1_1.DSN	Revision: 1.1
Date: Wednesday, February 04, 2009	Sheet 7 of 7	